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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Technology Center 2600

Applicants: Dominic J. GASBARRO et al.

Serial No.:   09/731,746

Filed: December 8, 2000

For: HOST-FABRIC ADAPTER HAVING BANDWIDTH-
                OPTIMIZING, AREA-MINIMAL, VERTICAL SLICED
                MEMORY ARCHITECTURE AND METHOD OF CONNECTING A
                HOST SYSTEM TO A CHANNEL-BASED SWITCHED FABRIC
                IN A DATA NETWORK

Art Unit: Unknown

Examiner: Not assigned

PRELIMINARY AMENDMENT

Assistant Commissioner of Patents   **January 17, 2001**
Washington, DC 20231

        Prior to examination, please amend the above-identified

application as follows:

        IN THE CLAIMS:

        Please **add new claims 21-24** as follows:

1   --21.   A method of storing data consisting of multiple widths

2   and multiple occurrences of data widths in a memory, comprising:

3   grouping the data widths of the same size and designating

4   as registers;

5   determining the number of registers;

6   selecting a number of vertically arranged memory slices of

7   registers of different sizes based on register width and system